Non-Commutative Circuits and the Sum-of-Squares Problem

[Extended Abstract]*

Pavel Hrubeš†
School of Mathematics
Institute for Advanced Study
Princeton, USA
pahrubes@gmail.com

Avi Wigderson†
School of Mathematics
Institute for Advanced Study
Princeton, USA
avi@ias.edu

Amir Yehudayoff†
School of Mathematics
Institute for Advanced Study
Princeton, USA
amir.yehudayoff@gmail.com

ABSTRACT

We initiate a direction for proving lower bounds on the size of non-commutative arithmetic circuits. This direction is based on a connection between lower bounds on the size of *non-commutative* arithmetic circuits and a problem about *commutative* degree four polynomials, the classical sum-of-squares problem: find the smallest n such that there exists an identity

$$(x_1^2+x_2^2+\cdots+x_k^2)\cdot(y_1^2+y_2^2+\cdots+y_k^2) = f_1^2+f_2^2+\cdots+f_n^2, \quad (1)$$

where each $f_i = f_i(X, Y)$ is bilinear in $X = \{x_1, \dots, x_k\}$ and $Y = \{y_1, \dots, y_k\}$. Over the complex numbers, we show that a sufficiently strong *super-linear* lower bound on n in (1), namely, $n \geq k^{1+\epsilon}$ with $\varepsilon > 0$, implies an *exponential* lower bound on the size of arithmetic circuits computing the non-commutative permanent.

More generally, we consider such sum-of-squares identities for any biquadratic polynomial $h(X, Y)$, namely

$$h(X, Y) = f_1^2 + f_2^2 + \cdots + f_n^2. \quad (2)$$

Again, proving $n \geq k^{1+\epsilon}$ in (2) for *any* explicit h over the complex numbers gives an *exponential* lower bound for the non-commutative permanent. Our proofs relies on several new structure theorems for non-commutative circuits, as well as a non-commutative analog of Valiant's completeness of the permanent.

We proceed to prove such super-linear bounds in some restricted cases. We prove that $n \geq \Omega(k^{6/5})$ in (1), if $f_1, \dots, f_n$ are required to have *integer* coefficients. Over the *real* numbers, we construct an explicit biquadratic polynomial h such that n in (2) must be at least $\Omega(k^2)$. Unfortunately, these results do not imply circuit lower bounds.

We also present other structural results about non-commutative arithmetic circuits. We show that any non-commutative circuit computing an *ordered* non-commutative polynomial can be efficiently transformed to a syntactically multilinear circuit computing that polynomial. The permanent, for example, is ordered. Hence, lower bounds on the size of syntactically multilinear circuits computing the permanent imply unrestricted non-commutative lower bounds. We also prove an exponential lower bound on the size of non-commutative syntactically multilinear circuit computing an explicit polynomial. This polynomial is, however, not ordered and an unrestricted circuit lower bound does not follow.

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Categories and Subject Descriptors

F.2.1 [**Theory of computation**]: Numerical Algorithms and Problems

General Terms

Theory

1. INTRODUCTION

1.1 Non-commutative computation

Arithmetic complexity theory studies computation of formal polynomials over some field or ring. Most of this theory is concerned with computation of commutative polynomials. The basic model of computation is that of *arithmetic circuit.* Despite decades of work, the best size lower bound for general circuits computing an explicit n-variate polynomial of degree d is $\Omega(n \log d)$, due to Baur and Strassen [29, 2]. Better lower bounds are known for a variety of more restricted computational models, such as monotone circuits, multilinear or bounded depth circuits (see, e.g., [6, 3]).

In this paper we deal with a different type of restriction. We investigate *non-commutative* polynomials and circuits; the case when the variables do not multiplicatively commute, i.e., $xy \neq yx$ if $x \neq y$, as in the case when the variables represent matrices over a field[1]. In a non-commutative circuit, a multiplication gate is given with an order in which its inputs are multiplied. Precise definitions appear in Section 2. A simple illustration of how absence of commutativity limits computation is the polynomial $x^2 - y^2$. If x, y commute,

[1]As in this case, addition remains commutative, as well as multiplication by constants.

the polynomial can be computed as $(x-y)(x+y)$ using one multiplication. In the non-commutative case, two multiplications are required to compute it.

Surprisingly, while interest in non-commutative computations goes back at least to 1970 [32], no better lower bounds are known for general non-commutative circuits than in the commutative case. The seminal work in this area is [21], where Nisan proved exponential lower bounds on non-commutative *formula* size of determinant and permanent. He also gives an explicit polynomial that has linear size non-commutative circuits but requires non-commutative formulas of exponential size, thus separating non-commutative formulas and circuits.

One remarkable aspect of non-commutative computation is its connection with the celebrated approximation scheme for the (commutative) permanent [14]. The series of papers [7, 16, 1, 5] reduce the problem of approximating permanent to the problem of computing determinant of a matrix whose entries are elements of (non-commutative) Clifford algebras. However, already in the case of quaternions (the third Clifford algebra), determinant cannot be efficiently computed by means of arithmetic formulas. This was shown by Chien and Sinclair [4] who extend Nisan's techniques to this and other non-commutative algebras.

In this paper, we propose new directions towards proving lower bounds on non-commutative circuits. We present structure theorems for non-commutative circuits, which enable us to reduce circuit size lower bounds to apparently simpler problems. The foremost such problem is the so called sum-of-squares problem, a classical question on a border between algebra and topology. We also outline a connection with multilinear circuits, in which exciting progress was made in recent years. We then make modest steps towards the lower-bound goal, and present results some of which are of independent interest. Before we describe the results, we take a detour to briefly describe the sum-of-squares problem and its long history.

1.2 The sum-of-squares problem

In this section all variables commute. Consider the polynomial

$$\mathsf{SOS}_k = (x_1^2 + x_2^2 + \cdots + x_k^2) \cdot (y_1^2 + y_2^2 + \cdots + y_k^2). \quad (3)$$

Given a field (or a ring) $\mathbb{F}$, define $\mathcal{S}_{\mathbb{F}}(k)$ as the smallest n such that there exists a polynomial identity

$$\mathsf{SOS}_k = z_1^2 + z_2^2 + \cdots + z_n^2\,, \quad (4)$$

where each $z_i = z_i(X,Y)$ is a bilinear form in variables $X = \{x_1, \dots, x_k\}$ and $Y = \{y_1, \dots, y_k\}$ over the field $\mathbb{F}$.

We refer to the problem of determining the value $\mathcal{S}_{\mathbb{F}}(k)$ as the *sum-of-squares* problem. Note that the problem is not interesting if $\mathbb{F}$ has characteristic two, for then $\mathcal{S}_{\mathbb{F}}(k) = 1$. Over other fields, the trivial bounds are

$$k \le \mathcal{S}_{\mathbb{F}}(k) \le k^2\,.$$

In Section 1.3, we describe the connection between the sum-of-squares problem and arithmetic complexity. At this point, let us discuss the mathematical significance of the sum-of-squares problem (much more can be found, e.g., in [28]). We focus on real sums of squares, for they are of the greatest historical importance[2]. Nontrivial identities exhibiting $\mathcal{S}_{\mathbb{R}}(k) = k$ initiated this story.

When $k = 1$, we have $x_1^2 y_1^2 = (x_1 y_1)^2$. When $k = 2$, we have

$$(x_1^2 + x_2^2) \cdot (y_1^2 + y_2^2) = (x_1y_1 - x_2y_2)^2 + (x_1y_2 + x_2y_1)^2.$$

Interpreting (x_1, x_2) and (y_1, y_2) as complex numbers α and β, this formula expresses the property

$$|\alpha|^2 |\beta|^2 = |\alpha\beta|^2 \quad (5)$$

of multiplication of complex numbers. The case $k = 1$ trivially expresses the same fact (5) for *real* α and β. In 1748, motivated by the number theoretic problem of expressing every integer as a sum of four squares, Euler proved an identity showing that $\mathcal{S}_{\mathbb{R}}(4) = 4$. When Hamilton discovered the *quaternion* algebra in 1843, this identity was quickly realized to express (5) for mutiplying quaternions. This was repeated in 1848 with the discovery of the *octonions* algebra, and the 8-square identity expressing (5) for octonions. Motivated by the study of division algebras, mathematicians tried to prove a 16-square identity in the following 50 years. Finally Hurwitz in 1898 proved that it is impossible, obtaining the first nontrivial lower bound:

THEOREM 1.1. ([11]) *$\mathcal{S}_{\mathbb{R}}(k) > k$, except if $k \in \{1, 2, 4, 8\}$.*

The following interpretation of the sum-of-squares problem got topologists interested in this problem: if $z_1, \dots, z_n$ satisfy (4), the map $z = (z_1, \dots, z_n) : \mathbb{R}^k \times \mathbb{R}^k \to \mathbb{R}^n$ is a bilinear *normed* map. Namely, it satisfies $|z(\bar{x}, \bar{y})| = |\bar{x}||\bar{y}|$ for every $\bar{x}, \bar{y} \in \mathbb{R}^k$, where $|\cdot|$ is the Euclidean norm. This rigid structure allows for topological and algebraic geometry tools to yield the following, best known lower bound, which unfortunately gains only a factor of two over the trivial bound:

THEOREM 1.2. ([13, 18]) *$\mathcal{S}_{\mathbb{R}}(k) \ge (2 - o(1))k$.*

As it happens, the trivial upper bound can be improved as well. There exists a normed bilinear map as above from $\mathbb{R}^k \times \mathbb{R}^{\rho(k)}$ to $\mathbb{R}^k$, with $\rho(k) = \Theta(\log k)$. This was shown by Radon and Hurwitz [24, 12], who computed the exact value of the optimal $\rho(k)$. Interestingly, such a map exists even if we require the polynomials z_i to have *integer*[3] coefficients, see [35, 19]. The existence of this integer bilinear normed map turns out to be related to Clifford algebras as well: it can be obtained using a matrix representation of a Clifford algebra with $\rho(k)$ generators. This can be seen to imply

FACT 1.3. $\mathcal{S}_{\mathbb{Z}}(k) \le O(k^2/\log k)$.

This is the best known upper bound on $\mathcal{S}_{\mathbb{R}}$, or $\mathcal{S}_{\mathbb{F}}$ for any other field with char $\mathbb{F} \ne 2$. This motivated researchers to study integer sums of squares, and try to prove lower bounds on $\mathcal{S}_{\mathbb{Z}}$. Despite the effort [18, 33, 28], the asymptotic bounds on $\mathcal{S}_{\mathbb{Z}}$ remained as wide open as in the case of reals. One of the contributions of this paper is the first super-linear lower bound in the integer case. We show that $\mathcal{S}_{\mathbb{Z}}(k) \ge \Omega(k^{6/5})$.

To illustrate the subtlety of proving lower bounds on the sum-of-squares problem, let us mention that if we allow the z_i's to be *rational* functions rather than polynomials, the

[2]The assumption that the z_i's in (4) are bilinear is satisfied automatically if the z_i's are real polynomials.

[3]The coefficients of the z_i's can actually be taken to be in $\{-1, 0, 1\}$.

nature of the problem significantly changes. In 1965, Pfister [23] proved that if the z_i's are rational functions, SOS_k can be written as a sum of k squares whenever k is a power of two.

1.3 Non-commutative circuits and bilinear complexity

Conditional lower bounds on circuit complexity.

The connection between the sum-of-squares problem and non-commutative lower bounds is that a sufficiently strong lower bound on $\mathcal{S}(k)$ implies an exponential lower bound for permanent. Here we present our main results, for a more detailed discussion, see Section 2.1. In the non-commutative setting, there are several options to define the permanent, we define it row-by-row, that is,

$$\mathrm{PERM}_n(X) = \sum_{\pi} x_{1,\pi(1)} x_{2,\pi(2)} \cdots x_{n,\pi(n)},$$

where π is a permutation of $[n] = \{1, \ldots, n\}$. The advertised connection can be summarized as follows[4].

THEOREM 1.4. *Let $\mathbb{F}$ be an algebraically closed field. Assume that $\mathcal{S}_{\mathbb{F}}(k) \geq \Omega(k^{1+\varepsilon})$ for a constant $\varepsilon > 0$. Then PERM_n requires non-commutative circuits of size $2^{\Omega(n)}$.*

Theorem 1.4 is an instance of a general connection between non-commutative circuits and commutative degree four polynomials, which we now proceed to describe.

Let f be a *commutative* polynomial of degree four over a field $\mathbb{F}$. We say that f is *biquadratic* in variables $X = \{x_1, \ldots, x_k\}$ and $Y = \{y_1, \ldots, y_k\}$, if every monomial in f has the form $x_{i_1} x_{i_2} y_{j_1} y_{j_2}$. If f is biquadratic in variables X and Y, we define

sum-of-squares complexity: $\mathcal{S}_{\mathbb{F}}(f)$ is the smallest[5] n so that f can be written as

$$f = z_1^2 + \cdots + z_n^2,$$

bilinear complexity: $\mathcal{B}_{\mathbb{F}}(f)$ is the smallest n so that f can be written as

$$f = z_1 z_1' + \cdots + z_n z_n',$$

where each z_i and z_i' are bilinear forms in X, Y. We thus have $\mathcal{S}_{\mathbb{F}}(\mathsf{SOS}_k) = \mathcal{S}_{\mathbb{F}}(k)$, as defined in the previous section.

Let us first note that over certain fields, $\mathcal{S}_{\mathbb{F}}(f)$ and $\mathcal{B}_{\mathbb{F}}(f)$ are virtually the same:

REMARK 1.5. *Clearly, $\mathcal{B}_{\mathbb{F}}(f) \leq \mathcal{S}_{\mathbb{F}}(f)$. If $\mathbb{F}$ is algebraically closed with char $\mathbb{F} \neq 2$, then $\mathcal{S}_{\mathbb{F}}(f) \leq 3\mathcal{B}_{\mathbb{F}}(f)$. This holds since $2zz' = (z+z')^2 + (\sqrt{-1}z)^2 + (\sqrt{-1}z')^2$.*

We now define the non-commutative version of SOS_k: the non-commutative *identity polynomial* is

$$\mathsf{ID}_k = \sum_{i,j \in [k]} x_i y_j x_i y_j. \tag{6}$$

We show that a lower bound on $\mathcal{B}_{\mathbb{F}}(\mathsf{SOS}_k)$ implies a lower bound on the size of non-commutative circuit computing ID_k.

[4]If char $\mathbb{F} = 2$, the theorem holds trivially, since $\mathcal{S}_{\mathbb{F}}(k) = 1$.
[5]When no such n exists, $\mathcal{S}_{\mathbb{F}}(f)$ is infinite.

THEOREM 1.6. *Over a field $\mathbb{F}$, the size of a non-commutative circuit computing ID_k is at least $\Omega(\mathcal{B}_{\mathbb{F}}(\mathsf{SOS}_k))$.*

Theorem 1.6 is proved in Section 4. The lower bound given by the theorem is reminiscent of the tensor rank approach to lower bounds for commutative circuits, where a lower bound on tensor rank implies circuit lower bounds [30]. In the non-commutative case we can prove a much stronger implication. For every $\varepsilon > 0$, a $k^{1+\varepsilon}$ lower bound on $\mathcal{B}_{\mathbb{F}}(\mathsf{SOS}_k)$ gives an exponential lower bound for the permanent. Theorem 1.7, which is proved in Section 5, together with Remark 1.5 imply Theorem 1.4.

THEOREM 1.7. *Assume that $\mathcal{B}_{\mathbb{F}}(\mathsf{SOS}_k) \geq \Omega(k^{1+\varepsilon})$, for some $\varepsilon > 0$. Then PERM_n requires non-commutative circuits of size $2^{\Omega(n)}$ over $\mathbb{F}$.*

The theorem is reminiscent of a result in Boolean complexity, where a sufficient *linear* lower bound on complexity of a bipartite graph implies an *exponential* circuit lower bound for a related function (see [15] for discussion.)

An important property that the non-commutative permanent shares with its commutative counterpart is its completeness for the class of explicit polynomials. This enables us to generalize Theorem 1.7 to the following theorem, which is proved in Section 5.1. Let $\{f_k\}$ be a family of commutative biquadratic polynomials such that the number of variables in f_k is polynomial in k. We call $\{f_k\}$ *explicit*, if there exists a polynomial-time algorithm which, given k and a degree-four monomial α as inputs[6], computes the coefficient of α in f_k. The polynomial SOS_k is clearly explicit.

THEOREM 1.8. *Let $\mathbb{F}$ be a field such that char $\mathbb{F} \neq 2$. Let $\{f_k\}$ be a family of explicit biquadratic polynomials. Assume that $\mathcal{B}_{\mathbb{F}}(f_k) \geq \Omega(k^{1+\epsilon})$ for some $\epsilon > 0$. Then PERM_n requires non-commutative circuits of size $2^{\Omega(n)}$ over $\mathbb{F}$.*

Lower bounds on sum-of-squares complexity in special cases.

Remark 1.5 tells us that for some fields, $\mathcal{B}_{\mathbb{F}} = \Theta(\mathcal{S}_{\mathbb{F}})$, and hence to prove a circuit lower bound, it is sufficient to prove a lower bound on $\mathcal{S}_{\mathbb{F}}$. We prove lower bounds on $\mathcal{S}_{\mathbb{F}}(k)$ in some restricted cases. For more details, see Section 2.2.

Over $\mathbb{R}$, we find an explicit 'hard' polynomial

THEOREM 1.9. *There exists an explicit family $\{f_k\}$ of real biquadratic polynomials with coefficients in $\{0,1,2,4\}$ such that $\mathcal{S}_{\mathbb{R}}(f_k) = \Theta(k^2)$.*

By Theorem 1.8, if the construction worked over the complex numbers $\mathbb{C}$ instead of $\mathbb{R}$, we would have an exponential lower bound on the size of non-commutative circuits for the permanent. Such a construction is not known.

We investigate sums of squares over integers. We prove the following:

THEOREM 1.10. $\mathcal{S}_{\mathbb{Z}}(k) \geq \Omega(k^{6/5})$.

This result, too, does not imply a circuit lower bound. However, if we knew how to prove the same for $\mathbb{Z}[\sqrt{-1}]$ instead of $\mathbb{Z}$, we would get lower bounds for circuits over $\mathbb{Z}$. Such lower bounds are not known.

[6]We think of the input as given in a binary representation; the algorithm thus runs in time polynomial in $\log k$.

1.4 Ordered and multilinear circuits

An important restriction on computational power of circuits is multilinearity. This restriction has been extensively investigated in the commutative setting. A polynomial is multilinear, if every variable has individual degree at most one in it. Syntactically multilinear circuits are those in which every product gate multiplies gates with disjoint sets of variables. This model was first considered in [22], where lower bounds on constant depth multilinear circuits were proved (and later improved in [26]). In a breakthrough paper, Raz [25] proved super-polynomial lower bounds on multilinear formula size for the permanent and determinant. These techniques were extended by [27] to give a lower bound of about $n^{4/3}$ for the size multilinear *circuits*.

An interesting observation about non-commutative circuits is that if they compute a polynomial of a specific form, they are without loss of generality multilinear. Let us call a non-commutative polynomial f *ordered*, if the variables of f are divided into disjoint sets $X_1, \dots, X_d$ and every monomial in f has the form $x_1 \cdots x_d$ with $x_i \in X_i$. The non-commutative permanent, as defined above, is thus ordered. An *ordered circuit* is a natural model for computing ordered polynomials. Roughly, we require every gate to take variables from the sets X_i in the same interval $I \subset [d]$. One property of ordered circuits is that they are automatically syntactically multilinear.

We show that any non-commutative circuit computing an ordered polynomial can be efficiently transformed to an ordered circuit, hence a multilinear one, computing the same polynomial. Such a reduction is not known in the commutative case, and gives hope that a progress on multilinear lower bounds for permanent or determinant will yield general non-commutative lower bounds.

THEOREM 1.11. *Let f be an ordered polynomial of degree d. If f is computed by a non-commutative circuit of size s, it can be computed by an ordered circuit of size $O(d^3 s)$.*

Again, we fall short of utilizing this connection for general lower bounds. By a simple argument, we manage to prove an exponential lower bound on non-commutative multilinear circuits, as we state in the next theorem. However, the polynomial AP_k in question is not ordered, and we cannot invoke the previous result to obtain an unconditional lower bound. (Theorem 1.12 is proved in Section 6).

THEOREM 1.12. *Let*

$$\mathsf{AP}_k = \sum_{\sigma} x_{\sigma(1)} x_{\sigma(2)} \cdots x_{\sigma(k)},$$

where σ is a permutation of $[k]$. Then every non-commutative multilinear circuit computing AP_k is of size at least $2^{\Omega(k)}$.

1.5 A different perspective: lower bounds using rank

An extremely appealing way to obtain lower bounds is by using sub-additive measures, and matrix rank is perhaps the favorite measure across many computational models. It is abundant in communication complexity, and in circuit complexity it has also found its applications. Often, one cannot hope to find a unique matrix whose rank would capture the complexity of the investigated function. Instead, we can associate the function with a family of matrices, and the complexity of the function is related to the *minimum* rank of matrices in that family. Typically, the family consists of matrices which are in some sense "close" to some fixed matrix.

For arithmetic circuits, many of the known structure theorems [8, 21, 25, 9] invite a natural rank interpretation. This interpretation, however, has lead to lower bounds only for restricted circuits. We sketch below the rank problem which arises in the case of commutative circuits, and explain why it is considerably simpler in the case of non-commutative ones.

Let f be a commutative polynomial of degree d. Consider $N \times N$ matrices whose entries are elements of some field, andÊ rows and columns are labelled by monomials of degree roughly $d/2$. Hence N is in general exponential in the degree of f. Associate with f a family $\mathcal{M}$ of all $N \times N$ matrices M with the following property: for every monomial α of degree d, the sum of all entries M_{β_1,β_2}, such that $\beta_1\beta_2 = \alpha$, is equal to the coefficient of α in f. In other words, we partition M into subsets T_α corresponding to the possible ways to write α as a product of two monomials, and we impose a condition on the sum of entries in every T_α. It can be shown that the circuit complexity of f can be lower bounded by the minimal rank of the matrices in $\mathcal{M}$.

Note that the sets T_α are of size exponential in d, the degree of f. The structure of the sets is not friendly either. Our first structure theorem for non-commutative circuits, which decomposes non-commutative polynomials to central polynomials, translates to a similar rank problem. However, the matrices $M \in \mathcal{M}$ will be partitioned into sets of size only d (instead of exponential in d). This is thanks to the fact that there are much fewer options to express a non-commutative monomial as a product of other monomials. Our second structure theorem, concerning block-central polynomials, gives a partition into sets of size at most two. The structure of these sets is quite simple too. However, not simple enough to allow us to prove a rank lower bound. In the rank formulation of circuit lower bounds, we can therefore see non-commutative circuits as a first step towards understanding commutative circuit lower bounds.

2. OVERVIEW OF PROOFS

We now outline proofs of the main theorems of the paper. Theorems 1.4 - 1.8 will be proved in Sections 3 - 5, and Theorem 1.12 in Section 6. Proofs of the rest of the theorems are omitted due to space restrictions.

2.1 Conditional lower bounds on non-commutative circuit size

In this section we describe the path that leads from non-commutative circuit complexity to bilinear complexity.

Preliminaries.

Let $\mathbb{F}$ be a field. A *non-commutative polynomial* is a formal sum of products of variables and field elements. We assume that the variables do not multiplicatively commute, that is, $xy \neq yx$ whenever $x \neq y$. However, the variables commute with elements of $\mathbb{F}$. The reader can imagine the variables as representing square matrices.

A *non-commutative arithmetic circuit* Φ is a directed acyclic graph as follows. Nodes (or gates) of in-degree zero are labelled by either a variable or a field element in $\mathbb{F}$. All the other nodes have in-degree two and they are labelled by either + or ×. The two edges going into a gate v labelled by × are labelled by *left* and *right*. We denote by $v = v_1 \times v_2$ the fact that (v_1, v) is the left edge going into v, and (v_2, v) is the right edge going into v. (This is to determine the order of multiplication.) The *size* of a circuit Φ is the number of edges in Φ. The integer $\mathcal{C}(f)$ is the size of a smallest circuit computing f.

Note. Unless stated otherwise, we refer to non-commutative polynomials as polynomials, and to non-commutative circuits as circuits.

The proof is presented in three parts, which are an exploration of the structure of non-commutative circuits.

Part I: structure of circuits.

The starting point is the structure of polynomials computed by non-commutative circuits, which we now explain. The methods we use are elementary, and are an adaptation of works like [8, 9] to the non-commutative world.

We start by defining the 'building blocks' of polynomials, which we call central polynomials. A homogeneous[7] polynomial f of degree d is called *central*, if there exist integers m and d_0, d_1, d_2 satisfying $d/3 \le d_0 < 2d/3$ and $d_0 + d_1 + d_2 = d$ so that

$$f = \sum_{i \in [m]} h_i g \overline{h}_i, \tag{7}$$

where

- (i). the polynomial g, which we call the *body*, is homogeneous of degree $\deg g = d_0$,
- (ii). for every $i \in [m]$, the polynomials $h_i, \overline{h}_i$ are homogeneous of degrees $\deg h_i = d_1$ and $\deg \overline{h}_i = d_2$.

The *width* of a homogeneous polynomial f of degree d, denoted $w(f)$, is the smallest integer n so that f can be written as

$$f = f_1 + f_2 + \cdots f_n, \tag{8}$$

with each f_i a central polynomial. In Section 3.1 we show that the width of f is at most $O(d^3 \mathcal{C}(f))$, and so lower bounds on width imply lower bounds on circuit complexity. We prove this by induction on the circuit complexity of f.

Part II: degree-four.

In the first part, we argued that a lower bound on width implies a lower bound on circuit complexity. In the case of degree-four, a central polynomial has a very simple structure: d_0 is always 2, and so the body must reside in one of three places: left (when $d_1 = 0$), center (when $d_1 = 1$), and right (when $d_1 = 2$). For a polynomial of degree four, we can thus write (8) with n at most order $\mathcal{C}(f)$, and each f_i of this special form.

This observation allows us to relate width and bilinear complexity, as the following proposition shows. For a more general statement, see Proposition 4.1, which also shows that the width and bilinear complexity are in fact equivalent.

[7]Recall that a polynomial f is *homogeneous*, if all monomials with a non-zero coefficient in f have the same degree, and that circuit Φ is homogeneous, if every gate in Φ computes a homogeneous polynomial.

PROPOSITION 2.1. $w(\mathsf{ID}_k) \ge \mathcal{B}(\mathsf{SOS}_k)$.

Part I and Proposition 2.1 already imply Theorem 1.6, which states that a lower bound on bilinear complexity implies a lower bound on circuit complexity of ID_k.

Part III: general degree to degree-four.

The argument presented in the second step can imply at most a quadratic lower bound on circuit size. To get exponential lower bounds, we need to consider polynomials of higher degrees. We think of the degree of a degree-$4r$ polynomial as divided into 4 groups, for which we try to mimic the special structure from part II: A *block-central* polynomial is a central polynomial so that $d_0 = 2r$ and $d_1 \in \{0, r, 2r\}$. The structure of block-central polynomials is similar to the structure of degree-four central polynomials in that the body is of fixed degree and it has three places it can reside in: left (when $d_1 = 0$), center (when $d_1 = r$), and right (when $d_1 = 2r$). In Section 5 we show that a degree-$4r$ polynomial f can be written as a sum of at most $O(r^3 2^r \mathcal{C}(f))$ block-central polynomials.

We thus reduced the analysis of degree-$4r$ polynomials to the analysis of degree-four polynomial. This reduction comes with a price, a loss of a factor of 2^r. We note that this loss is necessary. The proof is a rather technical case distinction. The idea behind it is a combinatorial property of intervals in the set $[4r]$, which allows us to transform a central polynomial to a sum of 2^r block-central polynomials.

Here is an example of this reduction in the case of the identity polynomial. The *lifted identity polynomial*, LID_r, is the polynomial in variables z_0, z_1 of degree $4r$ defined by

$$\mathsf{LID}_r = \sum_{e \in \{0,1\}^{2r}} z_e z_e \,,$$

where for $e = (e_1, \ldots, e_{2r}) \in \{0,1\}^{2r}$, define $z_e = \prod_{i=1}^{2r} z_{e_i}$. The lifted identity polynomial is the high-degree counterpart of the identity polynomial, which allows us to prove that a super-linear lower bound implies an exponential one (the corollary is proved in Section 5):

COROLLARY 2.2. *If $\mathcal{B}(\mathsf{SOS}_k) \ge \Omega(k^{1+\epsilon})$ for some $\epsilon > 0$, then $\mathcal{C}(\mathsf{LID}_r) \ge 2^{\Omega(r)}$.*

To complete the picture, we show that LID_r is reducible to the permanent of dimension $4r$.

LEMMA 2.3. *There exists a matrix M of dimension $4r \times 4r$ whose nonzero entries are variables z_0, z_1 so that the permanent of M is LID_r.*

To prove the lemma, the matrix M is constructed explicitly, see Section 5. The conditional lower bound on the permanent, Theorem 1.7, follows from Corollary 2.2 and Lemma 2.3.

An important property that non-commutative permanent shares with its commutative counterpart is completeness for the class of explicit polynomials. This enables us to argue that a super-linear lower bound on the bilinear complexity of an explicit degree-four polynomial implies an exponential lower bound on permanent. In the commutative setting, this a consequence of the VNP completeness of permanent, as given in [31]. In the non-commutative setting, one can prove a similar result [10].

2.2 Lower bounds on sum-of-squares complexity in restricted cases

We now discuss the lower bounds for restricted sum-of-squares problems we prove: an explicit lower bound over $\mathbb{R}$ and a lower bound for SOS_k over integers.

We phrase the problem of lower bounding $\mathcal{S}_{\mathbb{R}}(g)$ in terms of matrices of real vectors. Let $V = \{\mathbf{v}_{i,j} : i,j \in [k]\}$ be a $k \times k$ matrix whose entries are vectors in $\mathbb{R}^n$. We call V a *vector matrix*, and n is called the *height* of V. The matrix V defines a biquadratic polynomial $f(V)$ in $X = \{x_1, \dots, x_k\}$ and $Y = \{y_1, \dots, y_k\}$ by

$$f(V) = \sum_{i_1 \le i_2, j_1 \le j_2} a_{i_1,i_2,j_1,j_2} x_{i_1} x_{i_2} y_{j_1} y_{j_2},$$

where a_{i_1,i_2,j_1,j_2} is equal to $\mathbf{v}_{i_1,j_1} \cdot \mathbf{v}_{i_2,j_2} + \mathbf{v}_{i_1,j_2} \cdot \mathbf{v}_{i_2,j_1}$, up to a small correction factor which is not important at this point. We can think of the coefficients as given by the permanent of the 2×2 sub-matrix[8] of V defined by i_1, i_2 and j_1, j_2.

The following lemma gives the connection between sum-of-squares complexity and vector matrices.

LEMMA 2.4. *Let g be a biquadratic polynomial. Then $\mathcal{S}_{\mathbb{R}}(g) \le n$ is equivalent to the existence a vector matrix V of height n so that $g = f(V)$.*

As long as it is finite, the height of a vector matrix for any polynomial does not exceed k^2, and a counting argument shows that this holds for "almost" all polynomials. The problem is to construct explicit polynomials that require large height. Even a super-linear lower bound seems non-trivial, since the permanent condition does not talk about inner products of pairs of vectors, but rather about the sum of inner products of two such pairs. We manage to construct an explicit polynomial which requires near-maximal height $\Omega(k^2)$. In our proof, the coefficients impose (through the 2×2 permanent conditions) either equality or orthognality constraints on the vectors in the matrix, and eventually the existence of many pairwise orthogonal ones. In a crucial way, we employ the fact that over $\mathbb{R}$, if two unit vectors have inner product one, they must be equal. This property[9] fails over $\mathbb{C}$, but it is still possible that even over $\mathbb{C}$ our construction has similar height (of course, if this turns out to be even $k^{1+\epsilon}$, we get an exponential lower bound for non-commutative circuits).

The construction, however, does not shed light on the classical sum-of-squares problem which is concerned specifically with the polynomial SOS_k. In the case of SOS_k, the conditions on the matrix V from Lemma 2.4 are especially nice and simple: (1) all vectors in V are unit vectors, (2) in each row and column the vectors are pairwise orthogonal, and (3) every 2×2 permanent (of inner products) must be zero.

As mentioned in the introduction, the best upper bounds for the sum-of-squares problem have *integer* coefficients, and so a lot of effort was invested into proving lower bounds in the integer case. Despite that, previously known lower bounds do not even reach $2k$. We prove the first super-linear lower bound, $\mathcal{S}_{\mathbb{Z}}(k) = \Omega(k^{6/5})$. Over integers, we take advantage of the fact that the unit vectors in V must have entries in $\{-1, 0, 1\}$ and there is exactly one nonzero entry in each vector. The nonzero coordinate can be thus thought of as a "color" in $[n]$, which is signed by plus or minus. This gives rise to the earlier studied notion of *intercalate matrices* (see, [33] and the book [28]). The integer sum-of-squares problem can thus be phrased in terms of minimizing the number of colors in a signed intercalate matrix, which can be approached as an elementary combinatorial problem.

Our strategy for proving the integer lower bound has three parts. The first step uses a simple counting argument to show that there must exist a sub-matrix in which one color appears in every row and every column. In the second step we show that the permanent conditions give rise to a "forbidden configuration" in such sub-matrices. In the last step we conclude that any matrix without this forbidden configuration must have many colors.

[8] In some cases, e.g., when $i_1 = i_2$, this matrix can become 1×2, 2×1 or even 1×1, but we still think of it as a 2×2 matrix. This is also where the correction factor comes from.

[9] Here, the inner product of two complex vectors a, b is $\sum_i a_i b_i$, rather than $\sum_i a_i \bar{b}_i$, with $\bar{b}$ the complex conjugate of b.

3. NON-COMMUTATIVE CIRCUITS

We use the following notation. For a node v in a circuit Φ, we denote by Φ_v the sub-circuit of Φ rooted at v. Every node v computes a polynomial $\widehat{\Phi}_v$ in the obvious way. A *monomial* α is a product of variables, and $\mathsf{COEF}_\alpha(f)$ is the coefficient of α in the polynomial f. Denote by $\deg f$ the degree of f, and if v is a node in a circuit Φ, denote by $\deg v$ the degree of $\widehat{\Phi}_v$.

3.1 Structure of non-commutative circuits

In this section we describe the structure of the polynomials computed by non-commutative circuits. The methods we use are elementary, and are an adaptation of works like [8, 9] to the non-commutative world.

We start by defining the 'building blocks' of polynomials, which we call central polynomials. Recall that a polynomial f is *homogeneous*, if all monomials with a non-zero coefficient in f have the same degree, and that circuit Φ is homogeneous, if every gate in Φ computes a homogeneous polynomial. A homogeneous polynomial f of degree d is called *central*, if there exist integers m and d_0, d_1, d_2 satisfying

$$d/3 \le d_0 < 2d/3 \quad \text{and} \quad d_0 + d_1 + d_2 = d$$

so that

$$f = \sum_{i \in [m]} h_i g \bar{h}_i, \tag{9}$$

where

(i). the polynomial g is homogeneous of degree $\deg g = d_0$,

(ii). for every $i \in [m]$, the polynomials $h_i, \bar{h}_i$ are homogeneous of degrees $\deg h_i = d_1$ and $\deg \bar{h}_i = d_2$.

REMARK 3.1. *In the definition of central polynomial, no assumption on the size of m is made. Hence we can without loss of generality assume that $h_i = c_i \alpha_i$ and $\bar{h}_i = \beta_i$, where α_i is a monomial of degree d_1, β_i is a monomial of degree d_2, and c_i is a field element.*

The *width* of a homogeneous polynomial f of degree d, denoted $w(f)$, is the smallest integer n so that f can be written as

$$f = f_1 + f_2 + \cdots + f_n,$$

where $f_1, \ldots, f_n$ are central polynomials of degree d. The following proposition shows that the width of a polynomial is a lower bound for its circuit complexity. We will later relate width and bilinear complexity.

Proposition 3.2. *Let f be a homogeneous polynomial of degree $d \geq 2$. Then*

$$\mathcal{C}(f) \geq \Omega(d^{-3} w(f)).$$

Proof. We start by observing that the standard homogenization of commutative circuits [30, 3] works also for non-commutative circuits.

Lemma 3.3. *Let g be a homogeneous polynomial of degree d. Then there exists a homogeneous circuit of size $O(d^2 \mathcal{C}(f))$ computing g.*

Assume that we have a homogeneous circuit Φ of size s computing f. We will show that $w(f) \leq ds$. By Lemma 3.3, this implies that $w(f) \leq O(d^3 \mathcal{C}(f))$, which completes the proof. Without loss of generality, we can also assume that no gate v in Φ computes the zero polynomial (gates that compute the zero polynomial can be removed, decreasing the circuit size).

For a multiset of pairs of polynomials $\mathcal{H} = \{\langle h_i, \overline{h}_i \rangle : i \in [m]\}$, define

$$g \times \mathcal{H} = \sum_{i \in [m]} h_i g \overline{h}_i \,.$$

Let $\mathcal{G} = \{g_1, \ldots, g_t\}$ be the set of homogeneous polynomials g of degree $d/3 \leq \deg g < 2d/3$ so that there exists a gate in Φ computing g. We show that for every gate v in Φ so that $\deg v \geq d/3$ there exist multisets of pairs of homogeneous polynomials $\mathcal{H}_1(v), \ldots, \mathcal{H}_t(v)$ satisfying

$$\widehat{\Phi}_v = \sum_{i \in [t]} g_i \times \mathcal{H}_i(v). \tag{10}$$

We prove (10) by induction on the depth of Φ_v. If $\deg(v) < 2d/3$ then $\widehat{\Phi}_v = g_i \in \mathcal{G}$ for some $i \in [t]$. Thus (10) is true, setting $\mathcal{H}_i(v) = \{\langle 1, 1 \rangle\}$ and $\mathcal{H}_j(v) = \{\langle 0, 0 \rangle\}$ for $j \neq i$ in $[t]$. Otherwise, we have $\deg v \geq 2d/3$. When $v = v_1 + v_2$, we do the following. Since Φ is homogeneous, v_1, v_2 and v have the same degree which is at least $2d/3$. Induction thus implies: for every $e \in \{1, 2\}$,

$$\widehat{\Phi}_{v_e} \quad = \quad \sum_{i \in [t]} g_i \times \mathcal{H}_i(v_e).$$

This gives

$$\widehat{\Phi}_v = \widehat{\Phi}_{v_1} + \widehat{\Phi}_{v_2} = \sum_{i \in [t]} g_i \times \big(\mathcal{H}_i(v_1) \cup \mathcal{H}_i(v_2)\big).$$

When $v = v_1 \times v_2$, we have $\deg v = \deg v_1 + \deg v_2$. Since $\deg v \geq 2d/3$, either (a) $\deg v_1 \geq d/3$ or (b) $\deg v_2 \geq d/3$. In the case (a), by induction,

$$\widehat{\Phi}_{v_1} = \sum_{i \in [t]} g_i \times \mathcal{H}_i(v_1)\,.$$

Defining $\mathcal{H}_i(v) = \{\langle h, \overline{h}\widehat{\Phi}_{v_2} \rangle \ : \ \langle h, \overline{h} \rangle \in \mathcal{H}_i(v_1)\}$, we obtain

$$\widehat{\Phi}_v = \widehat{\Phi}_{v_1} \widehat{\Phi}_{v_2} = \Big(\sum_{i \in [t]} g_i \times \mathcal{H}_i(v_1)\Big)\widehat{\Phi}_{v_2} = \sum_{i \in [t]} g_i \times \mathcal{H}_i(v).$$

Since $\widehat{\Phi}_{v_2}$ is a homogeneous polynomial, $\mathcal{H}_i(v)$ consists of pairs of homogeneous polynomials. In case (b), let $\mathcal{H}_i(v) = \{\langle \widehat{\Phi}_{v_1} h, \overline{h} \rangle \ : \ \langle h, \overline{h} \rangle \in \mathcal{H}_i(v_2)\}$.

Applying (10) to the output gate of Φ, we obtain

$$f = \sum_{i \in [t]} g_i \times \mathcal{H}_i \,,$$

where $\mathcal{H}_i$ are multisets of pairs of homogeneous polynomials. For every $i \in [t]$ and every $r \leq d - \deg g_i$, define $\mathcal{H}_i^r = \{\langle h, \overline{h} \rangle \in \mathcal{H}_i \ : \ \deg(h) = r, \ \deg \overline{h} = d - \deg g_i - r\}$. Then $g_i \times \mathcal{H}_i^r$ is a central polynomial. Moreover, since f is homogeneous of degree d, we obtain

$$f = \sum_{i \in [t]} \sum_{r=0}^{d - \deg g_i} g_i \times \mathcal{H}_i^r \,.$$

Since $t \leq s$, the proof is complete. $\square$

3.2 Degree four polynomials

Before we describe the specific structure of degree four polynomials, let us give a general definition. Let $X_1, \ldots, X_r$ be (not necessarily disjoint) sets of variables. For a polynomial f, let $f[X_1, \ldots, X_r]$ be the homogeneous polynomial of degree r so that for every monomial α, we have: i) $\mathsf{COEF}_\alpha(f[X_1, \ldots, X_r]) = \mathsf{COEF}_\alpha(f)$, if $\alpha = x_1 x_2 \cdots x_r$ with $x_i \in X_i$ for every $i \in [r]$, and ii) $\mathsf{COEF}_\alpha(f[X_1, \ldots, X_r]) = 0$, otherwise.

We easily obtain the following refinement of structure of degree-four polynomials:

Lemma 3.4. *If $f = f[X_1, X_2, X_3, X_4]$, then $w(f)$ is the smallest n so that f can be written as $f = f_1 + \cdots + f_n$, where for every $t \in [n]$, either*

(a) $f_t = g_t[X_1, X_2] h_t[X_3, X_4]$, *or*

(b) $f_t = \sum_{i \in [m]} h_{t,i}[X_1] g_t[X_2, X_3] \overline{h}_{t,i}[X_4]$,

where $g_t, h_t, h_{t,i}, \overline{h}_{t,i}$ are some polynomials.

4. DEGREE FOUR AND BILINEAR COMPLEXITY

We consider polynomials of a certain structure. Let f be a polynomial in variables $X, Y = \{x_1, \ldots, x_k\}, \{y_1, \ldots, y_k\}$ so that $f = f[X, Y, X, Y]$, i.e.,

$$f = \sum_{i_1, j_1, i_2, j_2 \in [k]} a_{i_1, j_1, i_2, j_2} x_{i_1} y_{j_1} x_{i_2} y_{j_2} \,. \tag{11}$$

For a non-commutative polynomial g, we define $g^{(c)}$ to be the polynomial g understood as a commutative polynomial. For example, if $g = xy + yx$, then $g^{(c)} = 2xy$.

In particular, if f is of the form (11), the polynomial $f^{(c)}$ is biquadratic. In the following proposition, we relate the width of a polynomial f and $\mathcal{B}(f^{(c)})$.

Proposition 4.1. *Let f be a homogeneous polynomial of degree four of the form (11). Then $\mathcal{B}(f^{(c)}) \leq w(f)$.*

Proof. Using Lemma 3.4, we can write $f = f_1 + \cdots + f_n$, where for every $t \in [n]$, either

(a) $f_t = g_t[X, Y] h_t[X, Y]$, or

(b) $f_t = \sum_{i \in [m]} h_{t,i}[X] g_t[Y, X] \overline{h}_{t,i}[Y]$.

The commutative polynomial $f_t^{(c)}$ is a product of two bilinear forms in X and Y: in case (a), of $g_t[X,Y]^{(c)}$ and $h_t[X,Y]^{(c)}$, in case (b), of $g_t[Y,X]^{(c)}$ and $\sum_i h_{t,i}[X]\overline{h}_{t,i}[Y]$. Altogether $f^{(c)} = f_1^{(c)} + \cdots + f_n^{(c)}$, where each $f_t^{(c)}$ is a product of two bilinear forms, and hence $\mathcal{B}(f^{(c)}) \leq n$. □

PROOF OF THEOREM 1.6.. Recall the definition of the identity polynomial,

$$\mathsf{ID}_k = \sum_{i,j \in [k]} x_i y_j x_i y_j.$$

The commutative polynomial $\mathsf{ID}_k^{(c)}$ is the polynomial SOS_k

$$\mathsf{SOS}_k = \sum_{i \in [k]} x_i^2 \sum_{j \in [k]} y_j^2.$$

The theorem follows from Proposition 3.2 and 4.1. □

Let us note that it is not necessary to separate variables in ID_k into two disjoint sets X and Y. In the non-commutative setting, this is just a cosmetic detail:

REMARK 4.2. $w(\mathsf{ID}_k) = w(\sum_{i,j \in [k]} x_i x_j x_i x_j)$.

5. HIGHER DEGREES

In this section, we show that a sufficiently strong lower bound on the width of a degree four polynomial implies an exponential lower bound on the width, and hence circuit size, of a related high degree polynomial.

Let f be a homogeneous polynomial of degree $4r$. We assume that f contains only two variables z_0 and z_1. We define $f^{(\lambda)}$ to be the polynomial obtained by replacing degree r monomials in f by new variables. Formally, for every monomial α of degree r in variables z_0, z_1, introduce a new variable x_α. The polynomial $f^{(\lambda)}$ is defined as the homogenous degree four polynomial in the 2^r variables $X = \{x_\alpha : \deg \alpha = r\}$ satisfying

$$\mathsf{COEF}_{x_{\alpha_1} x_{\alpha_2} x_{\alpha_3} x_{\alpha_4}}(f^{(\lambda)}) = \mathsf{COEF}_{\alpha_1 \alpha_2 \alpha_3 \alpha_4}(f). \qquad (12)$$

REMARK 5.1. *Let g be a homogeneous degree four polynomial in k variables. If $k \leq 2^r$, then there exists a polynomial f of degree $4r$ in variables z_0, z_1 such that $g = f^{(\lambda)}$ (up to a renaming of variables).*

We now relate $w(f)$ and $w(f^{(\lambda)})$. To do so, we need a modified version of Proposition 3.2. Let f be a homogeneous polynomial of degree $4r$. We say that f is *block-central*, if either

I. $f = gh$, where g, h are homogeneous polynomials with $\deg g = \deg h = 2r$, or

II. $f = \sum_{i \in [m]} h_i g \overline{h}_i$, where $g, h_i, \overline{h}_i$ are homogeneous polynomials of degrees $\deg g = 2r$ and $\deg h_i, \deg \overline{h}_i = r$ for every $i \in [m]$.

Every block-central polynomial is also central. The following lemma shows that every central polynomial can be written as a sum of 2^r block-central polynomials. The lemma thus enables us to consider a simpler problem, i.e., lower bounding the width with respect to block-central polynomials. However, this simplification comes with a price, namely, a loss of a factor of 2^r.

LEMMA 5.2. *Let f be a central polynomial of degree $4r$ in two variables z_0, z_1. Then there exist $n \leq 2^r$ and block-central polynomials $f_1, \ldots, f_n$ so that $f = f_1 + \cdots + f_n$.*

PROOF. The proof is by a rather long case distinction, and we omit it. □

We can now relate the width of f and $f^{(\lambda)}$.

PROPOSITION 5.3. *Let f be a homogeneous polynomial of degree $4r$ in the variables z_0, z_1. Then $w(f) \geq 2^{-r} w(f^{(\lambda)})$.*

PROOF. Assume $w(f) = n$. Lemma 5.2 implies $f = f_1 + \cdots + f_{n'}$, where $n' \leq 2^r n$ and f_j are block-central polynomials. Equation (12) implies

$$f^{(\lambda)} = f_1^{(\lambda)} + \cdots + f_{n'}^{(\lambda)}.$$

It is thus sufficient to show that every $f_t^{(\lambda)}$ is a central polynomial, for then $w(f^{(\lambda)}) \leq n' \leq 2^r n$.

In order to do so, let us extend the definition of $(.)^{(\lambda)}$ as follows. If g is a polynomial of degree ℓr in the variables z_0, z_1, let $g^{(\lambda)}$ be the homogeneous polynomial of degree ℓ in X so that

$$\mathsf{COEF}_{x_{\alpha_1} \cdots x_{\alpha_k}}(g^{(\lambda)}) = \mathsf{COEF}_{\alpha_1 \cdots \alpha_k}(g).$$

If g, h are homogeneous polynomials whose degree is divisible by r, we obtain $(gh)^{(\lambda)} = g^{(\lambda)} h^{(\lambda)}$. Hence if $f_t = g_t h_t$ a block-centralpolynomial of type I, then $f_t^{(\lambda)} = g_t^{(\lambda)} h_t^{(\lambda)}$ is a central polynomial of type (a) according to Lemma 3.4 with $X = X_1 = X_2 = X_3 = X_4$. If $f_t = \sum_i h_{t,i} g_t \overline{h}_{t,i}$ is a block-centralpolynomial of type II, $f_t^{(\lambda)} = \sum_i h_{t,i}^{(\lambda)} g_t^{(\lambda)} \overline{h}_{t,i}{}^{(\lambda)}$, and hence $f_t^{(\lambda)}$ is a centralpolynomial of type (b) according to Lemma 3.4. □

By Remark 5.1, we can start with a degree four polynomial in $k \leq 2^r$ variables and "lift" it to a polynomial f of degree $4r$ such that $f^{(\lambda)} = g$. We can then deduce that a sufficiently strong lower bound on the bilinear complexity of g implies an exponential lower bound for the circuit complexity of f. We apply this to the specific case of the identity polynomial. The *lifted identity polynomial*, LID_r, is the polynomial in variables z_0, z_1 of degree $4r$ defined by

$$\mathsf{LID}_r = \sum_{e \in \{0,1\}^{2r}} z_e z_e ,$$

where for $e = (e_1, \ldots, e_s) \in \{0,1\}^s$, we define $z_e = \prod_{i=1}^s z_{e_i}$.

COROLLARY 5.4 (COROLLARY 2.2 RESTATED). *If $\mathcal{B}(\mathsf{SOS}_k) \geq \Omega(k^{1+\epsilon})$ for some $\epsilon > 0$, then $\mathcal{C}(\mathsf{LID}_r) \geq 2^{\Omega(r)}$.*

PROOF. The definition of LID_r can be equivalently written as

$$\mathsf{LID}_r = \sum_{e_1, e_2 \in \{0,1\}^r} z_{e_1} z_{e_2} z_{e_1} z_{e_2} .$$

By definition, $\mathsf{LID}_r^{(\lambda)} = \sum_{i,j \in [k]} x_i x_j x_i x_j$ with $k = 2^r$. Thus, by Remark 4.2, $w(\mathsf{LID}_r^{(\lambda)}) = w(\mathsf{ID}_k)$. By Proposition 5.3, $w(\mathsf{LID}_r) \geq 2^{-r} w(\mathsf{LID}_r^{(\lambda)})$. Hence $w(\mathsf{LID}_r) \geq 2^{-r} w(\mathsf{ID}_k)$. By Proposition 4.1, $w(\mathsf{ID}_k) \geq \mathcal{B}(\mathsf{ID}_k)$. If $\mathcal{B}(\mathsf{ID}_k) \geq c k^{1+\epsilon}$ for some constants $c, \epsilon > 0$, we have $w(\mathsf{LID}_r) \geq c 2^{-r} 2^{r(1+\epsilon)} = c 2^{\epsilon r}$. By Proposition 3.2, $\mathcal{C}(\mathsf{LID}_r) \geq \Omega(r^{-3} 2^{\epsilon r}) = 2^{\Omega(r)}$. □

One motivation for studying the lifted identity polynomial is that we believe it is hard for non-commutative circuits. However, note that an apparently similar polynomial has small circuit size. For $e = (e_1, \ldots, e_s) \in \{0,1\}^s$, let $e^\star = (e_s, \ldots, e_1)$. The polynomial

$$\sum_{e \in \{0,1\}^{2r}} z_e z_{e^\star} ,$$

has a non-commutative circuit of linear size. This result can be found in [21], where it is also shown that the non-commutative formula complexity of this polynomial is exponential in r.

We now show that LID_r is reducible to the permanent of dimension $4r$.

Lemma 5.5 (Lemma 2.3 restated). *There is a matrix M of dimension $4r \times 4r$ whose nonzero entries are variables z_0, z_1 so that the permanent of M is LID_r.*

Proof. For $j \in \{0,1\}$, let D_j be the $2r \times 2r$ matrix with z_j on the diagonal and zero everywhere else. The matrix M is defined as

$$M = \begin{bmatrix} D_0 & D_1 \\ D_1 & D_0 \end{bmatrix}.$$

The permanent of M taken row by row is

$$\mathsf{PERM}(M) = \sum_\sigma M_{1,\sigma(1)} M_{2,\sigma(2)} \cdots M_{4r,\sigma(4r)},$$

where σ is a permutation of $[4r]$. The permutations that give nonzero value in $\mathsf{PERM}(M)$ satisfy: for every $i \in [2r]$, if $\sigma(i) = i$ then $\sigma(2r+i) = 2r+i$, and if $\sigma(i) = 2r+i$ then $\sigma(2r+i) = i$. By definition of M, this means that for every such σ and $i \in [2r]$, $M_{i,\sigma(i)} = M_{i+2r,\sigma(i+2r)}$. Moreover, given the values of such a σ on $[2r]$, it can be uniquely extended to all of $[4r]$. □

Theorem 1.7 follows from Corollary 2.2 and Lemma 2.3.

5.1 Explicit polynomials and completeness of non-commutative permanent

We now turn to Theorem 1.8. Let $\{f_k\}$ be an infinite family of non-commutative polynomials over $\mathbb{F}$ so that every f_k has at most $p(k)$ variables and degree at most $p(k)$, where $p : \mathbb{N} \to \mathbb{N}$ is a polynomial. We call $\{f_k\}$ *explicit*, if there exists a polynomial time algorithm which, given k and a monomial α is input, computes $\mathsf{COEF}_\alpha(f_k)$. Hence PERM_k and other families of polynomials are explicit in this sense. In the commutative setting, the following theorem is a consequence of the VNP completeness of permanent, as given in [31]. In the non-commutative setting, one can prove a similar result:

Theorem 5.6. ([10]) *Assume that $\{f_k\}$ is an explicit family of non-commutative polynomials such that $\mathcal{C}(f_k) \geq 2^{\Omega(k)}$. Then $\mathcal{C}(\mathrm{PERM}_k) \geq 2^{\Omega(k)}$.*

Proof of Theorem 1.8. For a commutative biquadratic polynomial in k variables

$$f = \sum_{i_1,j_1,i_2,j_2 \in [k]} a_{i_1,j_1,i_2,j_2} x_{i_1} y_{j_1} x_{i_2} y_{j_2} ,$$

define f' as the non-commutative polynomial

$$f' = \sum_{i_1,j_1,i_2,j_2 \in [k]} a_{i_1,j_1,i_2,j_2} x_{i_1} y_{j_1} x_{i_2} y_{j_2} .$$

This is to guarantee that $f' = f'[X,Y,X,Y]$ and $(f')^{(c)} = f$ is as required in Proposition 4.1. Let r be the smallest integer so that $2^r \geq 2k$. Let $f^\star$ be the polynomial given by Remark 5.1 so that $(f^\star)^{(\lambda)} = f'$. If f is explicit, $f^\star$ is explicit.

Let $\{f_k\}$ be as in the assumption. As in the proof of Corollary 5.4, we conclude that $f_k^\star$ require exponential size non-commutative circuits. By Theorem 5.6, this implies an exponential lower bound for permanent. □

6. MULTILINEAR CIRCUITS

In this section we prove an exponential lower bound on the size of non-commutative syntactically multilinear circuits (a circuit Φ is *syntactically multilinear*, if for every product gate $v = v_1 \times v_2$ in Φ, the two circuits Φ_{v_1} and Φ_{v_2} do not share variables). Note that an ordered circuit is automatically syntactically multilinear. By means of Theorem 1.11, a lower bound on syntactically multilinear circuits computing an ordered polynomial would imply an unconditional lower bound. However, our lower bound involves a polynomial which is *not* ordered.

We now define the multilinear version of central polynomials. Let f be a multilinear polynomial of degree d. We say that f is *ml-central*, if f is central as in (9), and for every $i \in [m]$, the polynomial $h_i g \bar{h}_i$ is multilinear; in particular, the polynomials $h_i, g, \bar{h}_i$ have distinct variables.

The following lemma describes the structure of multilinear circuits.

Lemma 6.1. *Let f be a homogeneous multilinear polynomial of degree $d \geq 2$. Assume that there is a syntactically multilinear circuit Φ of size s computing f. Then there exist $n \leq O(d^3 s)$ and ml-central polynomials $f_1, \ldots, f_n$ such that $f = f_1 + \cdots + f_n$.*

Proof. Almost identical to Proposition 3.2. □

Our lower bound is based on counting monomials. The following lemma is the basic observation for the lower bound.

Lemma 6.2. *Let f be a ml-central polynomial of degree k in k variables. Then f has at most $2^{-\Omega(k)} k!$ monomials with nonzero coefficients.*

Proof. Write f as $f = \sum_{i \in [m]} h_i g \bar{h}_i$ with every $h_i g \bar{h}_i$ multilinear. Let X be the set of variables in f and X_0 the set of variables in g. Every monomial with a nonzero coefficient in f has the form $\alpha_1 \gamma \alpha_2$, where (1) γ is a multilinear monomial of degree d_0 in variables X_0, and (2) α_1, α_2 are multilinear monomials in the variables $X \setminus X_0$ of degrees d_1, d_2, and α_1,α_2 have distinct variables. Since $d_0 + d_1 + d_2 = k$, we have $|X_0| = d_0$. There are thus $d_0!$ βs in (1), and at most $(d_1+d_2)!$ pairs α_1, α_2 in (2). Hence f contains at most

$$d_0!(d_1+d_2)! = d_0!(k-d_0)! = \frac{k!}{\binom{k}{d_0}}$$

monomials with non-zero coefficients. Since $k/3 \leq d_0 < 2k/3$, this is at most $2^{-\Omega(k)} k!$. □

Define the all-permutations polynomial, AP_k, as a polynomial in variables $x_1, \ldots, x_k$

$$\mathsf{AP}_k = \sum_\sigma x_{\sigma(1)} x_{\sigma(2)} \cdots x_{\sigma(k)} ,$$

where σ is a permutation of $[k]$. Note that $\mathsf{AP}_k^{(ord)}$ is a polynomial in k^2 variables,

$$\mathsf{AP}_k^{(ord)} = \sum_{\sigma} x_{1,\sigma(1)} x_{2,\sigma(2)} \cdots x_{k,\sigma(k)}.$$

In other words, $\mathsf{AP}_k^{(ord)} = \mathrm{PERM}_k$.

Proof of Theorem 1.12.. Assume that AP_k has been computed by such a circuit of size s. By Lemma 6.1, AP_k can be written as a sum of $O(k^3 s)$ ml-central polynomials. By Lemma 6.2, AP_k can thus have at most $O(2^{-\Omega(k)} k! k^3 s)$ monomials with nonzero coefficients. However, AP_k has $k!$ monomials. □

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