LOWER BOUNDS FOR PARALLEL RANDOM-ACCESS MACHINES WITH UNBOUNDED SHARED MEMORY

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ABSTRACT

A parallel random-access machine (PRAM) consists of n synchronized processors and a shared memory. In this paper, we allow an infinite number of memory cells. The processors have arbitrary computational power and read and write access to the shared memory. In the literature, conflicts arising when several processors want to write into the same cell are resolved by various rules. We consider two such rules. In the COMMON PRAM, these processors have to write the same value and, in the PRIORITY PRAM, the processor with the smallest index succeeds. No lower bounds were previously known for these models. In this paper we prove two lower bounds.

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